SUMMARY This paper presents a successive cancellation (SC) decoding of polar codes modified for insertion/deletion/substitution (IDS) error channels, in which insertions and deletions are described by drift values. The recursive calculation of the original SC decoding is modified to include the drift values as stochastic variables. The computational complexity of the modified SC decoding is $O(D^3)$ with respect to the maximum drift value $D$, and $O(N \log N)$ with respect to the code length $N$. The symmetric capacity of polar bit channel is estimated by computer simulations, and frozen bits are determined according to the estimated symmetric capacity. Simulation results show that the decoded error rate of polar code with the modified SC list decoding is lower than that of existing IDS error correction codes, such as marker-based code and spatially-coupled code.

key words: insertion error, deletion error, synchronization error, polar code, successive cancellation decoding, list decoding

1. Introduction

Synchronization errors will be a major obstacle to improve the reliability of some types of future high-density memory and storage devices, such as, bit-patterned media recording [1], racetrack memory [2], [3], and DNA archival storage [4]. Hence, it is expected that error control codes capable of correcting synchronization errors will be crucial to improve the reliability of these devices. Channels with synchronization errors are usually modeled by insertion/deletion/substitution (IDS) channel, and several classes of IDS error correcting codes were proposed. For example, codes with the bounded distance decoding were designed based on a constraint on weighted sum of codeword bits, such as, single IDS error correcting code [5], multiple IDS error correcting code [6], and non-binary single IDS error correcting code [7]. Another strategy for IDS error correction is to employ the concatenated coding, where the outer code is a random error correcting code (e.g., LDPC code), and the inner code controls prior probabilities of channel input bits, such as watermark embedding [8] and marker insertion [9]. Also, it was shown that spatially-coupled codes can correct IDS errors without using the inner coding [10].

Arikan proposed polar coding with successive cancellation (SC) decoding [11], which can achieve the symmetric capacity of any binary-input memoryless channel with the computational complexity of $O(N \log N)$, where $N = 2^n$ is the code length. Şasoglu et al. proved that the polar code can achieve the capacity of any discrete memoryless channel [12]. To improve the performance of polar code with finite code length, Tal and Vardy presented SC list (SCL) decoding with cyclic redundancy check (CRC) [13]. For correction of synchronization errors using the polar code, Thomas et al. presented a polar coding for channels with erasures and deletions [14], where the coding mainly deals with single deletion errors. Tian et al. proposed an SC decoding of polar code for channels with $d$ deletions [15], and they proved that this coding achieves the symmetric capacity.

Existing IDS channels often assume that insertions and deletions occur probabilistically [3], [4], [8]–[10], as opposed to the deletion model in [15]. That is, received word may have multiple insertion and deletion errors simultaneously, and thus the numbers of insertions and deletions cannot be determined uniquely from the received word length $N'$. Hence, extension of the $d$-deletion correction polar coding [15] to the multiple insertion/deletion error correction coding will not be straightforward. From this, we present an SC decoding of polar codes for IDS channels in which insertions, deletions, and substitution errors occur with probability $p_i, p_d$, and $p_s$, respectively, under a constraint on maximum drift between transmitted and received bits. This paper employs an IDS channel model expressed by a sequence of drift values, which is suitable for deriving a modified recursive computation in the SC decoding. That is, the appropriate combination of the IDS channel model and recursive computation enables a natural extension of the existing SC decoding to IDS error correction. Simulation results show that the presented SC decoding has lower decoded error rates compared to the existing marker-based code and spatially-coupled code.

The rest of this paper is organized as follows. Section 2 describes notations used in this paper. Section 3 defines IDS channel model. Section 4 presents a polar coding with modified SC decoding for IDS channel. Section 5 shows simulation results, and Sect. 6 concludes this paper.

2. Notations

This paper uses the following notations. Finite subsets of integers are defined as $\mathbb{Z}_M = \{0, 1, \ldots, M - 1\}$ and $\mathbb{B} = \mathbb{Z}_2 = \{0, 1\}$. The inversion of bit $x$ is denoted as $\overline{x} = x \oplus 1$, where $\oplus$ is the XOR operator, and $x, \overline{x} \in \mathbb{B}$. For a binary vector $a = (a_0, a_1, \ldots, a_{N-1}) \in \mathbb{B}^N$ of length $N$, sub-vector $a_i^j$ is defined as

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where \( p \) is the vector of length zero. For a binary vector \( a = (a_0, a_1, \ldots, a_{2N-1}) \in \mathbb{B}^N \) of even-length \( 2N \), mappings even \((a)\) and odd \((a)\) are defined as follows:

\[
even (a) = (a_0, a_2, \ldots, a_{2i}, \ldots, a_{2N-2}),
\]

\[
odd (a) = (a_1, a_3, \ldots, a_{2i+1}, \ldots, a_{2N-1}).
\]

For two binary vectors, \( a = (a_0, a_1, \ldots, a_{N-1}) \in \mathbb{B}^N \) and \( b = (b_0, b_1, \ldots, b_{N-1}) \in \mathbb{B}^N \), XOR of \( a \) and \( b \) is defined as

\[
a \oplus b = (a_0 \oplus b_0, a_1 \oplus b_1, \ldots, a_{N-1} \oplus b_{N-1}).
\]

Probability that a random variable \( X \) takes a value \( x \) is simply denoted as \( p(x) = \Pr(X = x) \). Conditional and joint probabilities are denoted similarly, that is, \( p(x|y) = \Pr(X = x|Y = y) \) and \( p(x, y) = \Pr(X = x, Y = y) \).

### 3. Channel Model

Let \( x \) and \( y \) be transmitted and received words, respectively, where the words are expressed by binary vectors as

\[
x = x_0^{N-1} = (x_0, x_1, \ldots, x_{N-1}) \in \mathbb{B}^N
\]

\[
y = y_0^{N-1} = (y_0, y_1, \ldots, y_{N-1}) \in \mathbb{B}^N.
\]

Let \( p_s, p_d, \) and \( p_e \) be insertion, deletion, and substitution error probabilities, respectively. And let \( D \) be the maximum absolute value of drift between \( x \) and \( y \). Insertion/deletion errors between \( x \) and \( y \) are expressed by a drift vector

\[
d = (d_0, d_1, \ldots, d_{N-1}, d_N) \in \mathbb{D}^{N+1},
\]

where \( \mathbb{D} = \{-D, -D + 1, \ldots, 0, 1, \ldots, D\} \) is the set of drift values. We assume that the drift vector is determined by Markov process with the following state transition probabilities:

\[
p(d_{i+1}|d_i) = \begin{cases} p_i & (d_{i+1} = d_i + 1 \land (d_i \neq D)) \\ p_d & (d_{i+1} = d_i - 1 \land (d_i \neq -D)) \\ 1 - p_s - p_d & ((d_{i+1} = d_i) \land (-D < d_i < D)) \\ 1 - p_i & ((d_{i+1} = d_i) \land (d_i = D)) \\ 1 - p_d & ((d_{i+1} = d_i) \land (d_i = -D)) \\ 0 & (\text{otherwise}) \end{cases}
\]

where the initial drift value is \( d_0 = 0 \). Figure 1(a) shows the state transition diagram of \( d_i \). From this definition, \( d \) satisfies \(-1 \leq d_{i+1} - d_i \leq 1\) for all \( i \in \mathbb{Z}_N \). Let \( I(i; d_i, d_{i+1}) \) be a set of indices in received word \( y_0^{N-1} \) corresponding to the \( i \)-th transmitted bit \( x_i \), that is,

\[
I(i; d_i, d_{i+1}) = \{ i' \mid i + d_i \leq i' < (i + 1) + d_{i+1} \} \subset \mathbb{Z}_N,
\]

where \( N' = N + d_N \). The following relations hold for \( I(i; d_i, d_{i+1}) \):

Let \( N = 2^n \) be the code length of polar code. Let \( I \) and \( I^c \) be sets of information and frozen bits, respectively, where \( I \cup I^c = \mathbb{Z}_N \) and \( I \cap I^c = \emptyset \). The positions of frozen bits are determined based on a simulation based estimation described in Sect.4.3. It is assumed that the value of frozen bit is zero, while effect of frozen bit values on the decoded error rate will be examined by simulation in Sect.5.2. Encoding and decoding use \( n + 1 \) intermediate binary vectors, \( u(n), u(n - 1), \ldots, u(0) \in \mathbb{B}_N \), each of length \( N = 2^n \). Figure 2 illustrates notations in the encoding and decoding procedures.

#### 4.1 Encoding

Encoding process is identical to that of the original polar code. That is, information word \( u_0^{N-1} = u(n) \) is encoded as \( x_0^{N-1} = u(0) \) by the following recursive calculation:

\[
u(n) = u_0^{N-1},
\]
4.2 Successive Cancellation Decoding

The SC decoder is given the frozen bit set $\mathcal{R}^c \subset \mathbb{Z}_N$, value of each frozen bit, and channel parameters, $p_x, p_0, p_\beta$, and $D$. The decoder inputs received word $y_0^{N-1} \in \mathbb{B}^{N'}$ with its length $N'$, and it outputs decoded word $\hat{u}_0^{N-1} = (\hat{u}_0, \hat{u}_1, \ldots, \hat{u}_{N-1}) \in \mathbb{B}^N$. The original SC decoding is modified to deal with drift $d_i$, as follows.

The $i$-th information bit $u_i$ is estimated as

$$\hat{u}_i = \begin{cases} u_i & (i \notin \mathcal{R}^c) \\ h_i \left( N', y_0^{N-1}, \hat{u}_0^{i-1} \right) & (i \in \mathcal{R}^c) \end{cases}$$

where

$$h_i \left( N', y_0^{N-1}, \hat{u}_0^{i-1} \right) = \begin{cases} 0 & \left( p(d_N, y_0^{N-1}, u_i^{i-1}, \hat{u}_0^{i-1}, | u_i = 0 \rangle \geq 1 \right) \\ 1 & \text{(otherwise)} \end{cases}$$

We firstly consider a polar bit IDS channel of level $k = n$ for $u(n) = u_0^{N-1}$ defined as follows:

$$W_{2^k}^{(2j-1)} \left( d_N, y_0^{N-1}, u^{(i-1)} = \hat{u}_0^{i-1} \right) = p \left( d_N, y_0^{N-1}, u^{(i-1)} = \hat{u}_0^{i-1} \right) | d_0 = 0, u_i = c \rangle,$$

where $c \in \mathbb{B}$. This channel is factorized into two channels of $W_{2^k}^{(2j+1)} (\cdot)$ in level $k = n - 1$. Similarly, we can apply recursive factorization of the polar bit IDS channel in level $k$, $W_{2^k}^{(2j)} (\cdot)$, into two channels in level $k - 1$, $W_{2^{k-1}}^{(2j)} (\cdot)$. Unlike the existing SC decoding, the following SC decoding considers cases in which sub-vector of received word $y$ is factorized into two vectors of unequal lengths due to insertion and deletion errors. Figure 3 illustrates the polar bit IDS channel corresponding to level $k$.

4.2.1 Recursions for Level $k \in \{1, 2, \ldots, n\}$

Probabilities in level $k$ are calculated using probabilities in level $k - 1$. In level $k$, probability is calculated for each combination $(m, j)$ of block index $m \in \{0, 1, \ldots, 2^{n-k} - 1\}$ and bit index $j \in \{0, 1, \ldots, 2^{k-1} - 1\}$. The following notations are used in the recursion:

$$\alpha = 2^k m, \beta = 2^k (m + 1), \gamma = (\alpha + \beta) / 2$$

where

$$\bar{u} = (\bar{u}_0, \bar{u}_1, \ldots, \bar{u}_{2^k - 1}) = u(k - 1)^{\gamma - 1} \in \mathbb{B}^{2^{k-1}},$$

$$\bar{v} = (\bar{v}_0, \bar{v}_1, \ldots, \bar{v}_{2^k - 1}) = u(k - 1)^{\gamma - 1} \in \mathbb{B}^{2^{k-1}},$$

$$\bar{w} = (\bar{w}_0, \bar{w}_1, \ldots, \bar{w}_{2^k - 1}) = u(k - 1)^{\gamma - 1} \in \mathbb{B}^{2^{k-1}},$$

$$u'_i = \text{even} \left( \bar{u}_i^{2j-1} \right) \oplus \text{odd} \left( \bar{u}_i^{2j-1} \right) \in \mathbb{B}^j,$$

$$u'_{\gamma} = \text{odd} \left( \bar{u}_0^{2j-1} \right) \in \mathbb{B}^j.$$

The probabilities for bits of even indices are calculated as follows:

$$W_{2^{k-1}}^{(2j)} \left( d_N, y_0^{N-1}, u^{(i-1)} = u'_i \right) | d_0 = 0, u_i = c \rangle$$

where $\bar{u}_j = \bar{u}_j \oplus \bar{u}_{j+1} \oplus \bar{w}_j = \bar{u}_{j+1} + \frac{1}{2}.$
Those of odd indices are calculated as follows:

\[
W_{2k+1}(d, y_{\alpha}, y_{\alpha+1}, \beta, \gamma) = \left\{ \begin{array}{ll} \frac{1}{2} \sum_{d \in D} W_{2k-1}(d, y_{\alpha}, y_{\alpha+1}, \beta, \gamma) & \text{if } d_j \in \mathbb{D} \\
\frac{1}{2} \sum_{d \in D} W_{2k-1}(d, y_{\alpha}, y_{\alpha+1}, \beta, \gamma) & \text{otherwise}
\end{array} \right.
\]

The above calculations assume that the following relation holds:

\[
p\left(\tilde{d}_{2j+1} \mid \tilde{d}_{2j}\right) = p\left(\tilde{d}_{2j} \mid \tilde{d}_{2j+1}\right) = 1/2.
\]

4.2.2 Calculation for Level \(k = 0\)

For \(i \in \{0, 1, \ldots, N - 1\}\), the probability is calculated as

\[
W_{2i}(d_{i+1}, y_{i+1}, \beta, \gamma) = \left\{ \begin{array}{ll} p(\alpha_{i+1} \mid d_{i+1}, y_{i+1}) & \text{if } d_{i+1} \in \mathbb{D} \\
0 & \text{otherwise}
\end{array} \right.
\]

Here, the second factor of right-hand side is given by Eq. (1), and the first factor is calculated as

\[
p(\alpha_{i+1} \mid d_{i+1}, x_i) = \begin{cases} p_0^\delta (1 - p_0)^{1-\delta} & \left( |d_{i+1} - d_i| \leq 1, 0 \leq i + d_i, i + d_{i+1} < N' \right) \\ 0 & \text{otherwise} \end{cases}
\]

where \(\delta = |I(i; d_i, d_{i+1})|\) and

\[
\delta = \left| \{i' \in I(i; d_i, d_{i+1}) \mid y_{i'} \neq x_i\} \right| = \sum_{i' \in I(i; d_i, d_{i+1})} (x_{i'} \otimes y_{i'}).
\]

This SC decoding can be extended to the list decoding in the same way as the existing SCL decoding [13], and also concatenation with a CRC is straightforward.

4.3 Determination of Frozen Bits

Let \(I(W_{N}^{(i)})\) be the symmetric capacity of \(W_{N}^{(i)}\) defined as

\[
I(W_{N}^{(i)}) = \frac{1}{2N} \sum_{u \in \mathbb{B}} \sum_{y \in \mathbb{D}} p\left(\gamma_0 N^{(-1)} \mid u_0^{(-1)}\right) I\left(d_N, y_0 N^{(-1)}, u_0^{(-1)} \mid u_i\right).
\]

(2)

where \(\mathbb{B} = \cup_{d \in D} \mathbb{D}^{N+d}\) and

\[
\tilde{I}\left(d_N, y_0 N^{(-1)}, u_0^{(-1)} \mid u_i\right) = \log \frac{p\left(\gamma_0 N^{(-1)}, u_0^{(-1)} \mid u_i\right)}{\frac{1}{2} p(\gamma_0 N^{(-1)}, u_0^{(-1)} \mid u_i) + \frac{1}{2} p(d_N, y_0 N^{(-1)}, u_0^{(-1)} \mid \tilde{u}_i)}.
\]

(3)

For given code length \(N\) and rate \(R\), the set of positions of frozen bits is determined as

\[\mathcal{R}^c = \{i_0, i_1, \ldots, i_{m-1}\} \subset \mathbb{Z}_N,\]

where \(m = [NR]\) is the round-off of \(NR\), and

\[\forall i, j \in \mathbb{Z}_N, (i \in \mathcal{R}^c \land j \in \mathcal{R}^c) \rightarrow I(W_N^{(i)}) \leq I(W_N^{(j)})\]

For the polar bit IDs channel, it will be difficult to calculate the exact value of the symmetric capacity \(I(W_N^{(i)})\). Observing that Eq. (2) is the expectation of Eq. (3) over all received words \(y_0 N\) for a given codeword \(u_0 N\), where \(u\) is uniformly distributed, we use the following simulation based method to estimate the symmetric capacity.

1. Generate a random information vector \(u_0 N\).
2. Encode \(u_0 N\) to codeword \(x_0 N\).
3. Determine received word \(y_0 N\) probabilistically according to the IDS channel model.
4. Calculate \(\lambda = \tilde{I}\left(d_N, y_0 N^{(-1)}, u_0^{(-1)} \mid u_i\right)\) of Eq. (3) using the modified SC decoding.
5. Repeat the above procedure for a fixed number of times, and then determine the average of \(\lambda\) as the symmetric capacity.

4.4 Estimation of Block Error Rate

For given \(\mathcal{A} = \mathbb{Z}_N \setminus \mathcal{R}^c\), an upper bound on decoded block error rate (BLER) of the above SC decoding is derived as follows [11]:

\[
p(\mathcal{E}) \leq \sum_{i \in \mathcal{A}} p(\mathcal{E}_i),
\]

where \(\mathcal{E}_i\) is the event that the \(i\)-th bit is incorrectly decoded as \(\tilde{u}_i = \bar{u}_i\), \(\mathcal{E}\) is the event that a decoded word has at least one incorrectly decoded bit, and

\[
p(\mathcal{E}_i) = \frac{1}{2N} \sum_{u \in \mathbb{B}} \sum_{y \in \mathbb{D}} p\left(\gamma_0 N^{(-1)} \mid u_0^{(-1)}\right) F_i(d_N, y_0 N^{(-1)}, u_0^{(-1)} \mid u_i).
\]

(5)

Here, \(F_i(\cdot) \in \mathbb{B}\) is defined using the indicator function \(\mathbb{I}\) as

\[
F_i(d_N, y_0 N^{(-1)}, u_0^{(-1)} \mid u_i) = \mathbb{I}\left(\tilde{d}_N, \tilde{y}_0 N^{(-1)}, \tilde{u}_0^{(-1)} \mid u_i\right).
\]
\[
\begin{align*}
\begin{cases}
I(p(d_N, y_0^{N-1}, u_0^1 | \tilde{y}_0)) < p(d_N, y_0^{N-1}, u_0^1 | 1) & (u_i = 0) \\
p(d_N, y_0^{N-1}, u_0^1 | \tilde{y}_0) \leq p(d_N, y_0^{N-1}, u_0^1 | 0) & (u_i = 1)
\end{cases}
\end{align*}
\]

Similar to the estimation of symmetric capacity based on Eqs. (2) and (3), the value of \( p(\tilde{E}_i) \) is estimated by a simulation based method since Eq. (5) is the expectation of Eq. (6) over all received words \( y_0^{N-1} \) for a given codeword \( u^{N-1} \), where \( u \) is uniformly distributed. Numerical example of the estimated BLER will be shown in Fig. 8 of Sect. 5.2.

4.5 Computational Complexity

Recursion structure of the original SC decoding is expressed as follows:

\[
W^{(j)}(\cdot | \tilde{u}_j) = \frac{1}{2} \sum_{\tilde{u}_{j+1} \in \mathbb{B}} W^{(j)}(\cdot | \tilde{u}_{j+1}) W^{(j)}(\cdot | \tilde{u}_{j+1}) W^{(j)}(\cdot | \tilde{u}_{j+1})
\]

\[
W^{(j+1)}(d, \cdot | \tilde{u}_{j+1}) = \frac{1}{2} \sum_{d_j \in D} W^{(j)}(d_j, \cdot | \tilde{u}_{j+1}) W^{(j)}(d_j, \cdot | \tilde{u}_{j+1})
\]

where \( \tilde{u} = (\tilde{u}_0, \tilde{u}_1, \ldots, \tilde{u}_{N-1}) = u(k) \). The probability is calculated for each element \( \tilde{u}_i \) of each intermediate vector \( u(k) \), where \( i \in \mathbb{Z}_N \) and \( k \in \mathbb{Z}_n \). Hence computational complexity is \( O(N \log N) \). On the other hand, recursion of the presented SC decoding for IDS channel is expressed as follows:

\[
W^{(j)}(d, \cdot | d, \tilde{u}_j) =
\]

\[
\frac{1}{2} \sum_{d_j \in D} W^{(j)}(d_j, \cdot | d, \tilde{u}_{j+1}) W^{(j)}(d_j, \cdot | d, \tilde{u}_{j+1})
\]

where \( d, \tilde{u} \in D \). From the above relations, the number of calculated probabilities is increased by a factor of \( |D|^2 = (2D + 1)^2 \), and the complexity of calculation of each probability is increased by a factor of \( |D| = 2D + 1 \). Thus, the complexity of the presented SC decoding is \( O(D^3) \) with respect to the maximum drift value \( D \), while it is still \( O(N \log N) \) with respect to the code length \( N \).

5. Simulation Results

Numerical results of polarization of \( W^{(i)} \) and decoded error rates are presented in Sects. 5.1 and 5.2, respectively. We assume that the maximum drift values is \( D = 4 \) in the following simulations.

5.1 Channel Polarization

Figure 4 presents relation between the bit index \( i \) of \( W^{(i)} \) and symmetric capacity \( I(W^{(i)}_{\text{sym}}) \) estimated by the simulation described in Sect. 4.3, where \( p_1 = p_2 = 1.0 \times 10^{-3}, p_3 = 1.0 \times 10^{-2} \), and \( n = 12 \). This simulation result suggests polarization of \( W^{(i)}_{\text{sym}} \) similar to memoryless channels. For \( p_1 = p_2 = 5.0 \times 10^{-3} \) and \( p_3 = 0 \), Fig. 5 shows the relation...
between bit position (sorted by \( I(W_i^{[j]}) \) and normalized by code length \( N = 2^n \)) and estimated symmetric capacity, where \( n \in \{10, 11, \ldots, 15\} \). The result suggests that longer codes have stronger polarization effect, and it is expected that the rate \( \{i \in \mathbb{Z}_N \mid I(W_i^{[j]}) > 1 - \epsilon\} / N \) approaches the channel SIR, \( 9.31 \times 10^{-1} \). Figure 6 presents the case of channel with substitution errors, where \( p_s = 1.0 \times 10^{-2} \). This channel also shows the polarization effect similar to Fig. 5.

5.2 Block and Bit Error Rates

The block error rate (BLER) and bit error rate (BER) of the presented coding are evaluated by simulations, where the positions of frozen bits are determined according to the procedure of Sect. 4.3 with \( 10^4 \) iterations. The value of frozen bit is \( u_i = 0 \) for all \( i \in \mathbb{F}_q \), except in Paragraph (6) wherein the effect of frozen bit values on decoded error rate is examined. The CRC for SCL decoding is defined by generator polynomial \( g(x) = x^8 + x^7 + x^6 + x^4 + x^2 + 1 \).

(1) Relation to the Code Length \( N = 2^n \)

Figure 7 shows the BLER and BER for code lengths \( N = 2^n \), where \( n \in \{11, 12, 13, 14\} \). Here, the horizontal axis is the insertion/deletion probability \( p_i = p_s \), the substitution probability is \( p_s = 0 \), code rate \( R = 0.80 \), and list size \( L = 4 \).

(2) Relation to the List Size \( L \)

Figure 8 presents effect of the list size \( L \in \{1, 4, 8, 16\} \) on the decoded BLER and BER, where the code length is \( N = 2^{12} \) and code rate is \( R = 0.80 \). The results show that the SCL decoding has much lower error rate compared to the SC decoding (\( L = 1 \)), while the improvement diminishes for larger list sizes. This figure also shows the estimated BLER of SC decoding (\( L = 1 \)) given by the right-hand side of Eq. (4). The estimation gives accurate values of BLER for the simulated parameters.

(3) Comparison to Marker-Based Codings

Figure 9 compares the error rates of the polar codes with those of marker-based coding [9], where \( p_s = 0 \), \( n \in \{12, 13\} \), \( R = 0.80 \), and \( L = 16 \). The outer code of the marker-based coding is binary (3,6)-regular LDPC code of rate 0.88, and the inner code is insertion of marker (0,1) with the regular interval of 20 bits. The results show that the error rates
of polar code are lower than those of marker-based codings.

(4) Error Rates of Channels with Substitution Errors

Figure 10 presents the error rates of polar code and marker-based coding when the substitution error probability is $p_s = 1.0 \times 10^{-2}$, where the marker coding is with the interval of 96 bits. The polar codes have lower error rates compared to the marker-based coding, while discrepancy from the SIR ($= 1.05 \times 10^{-2}$) is large. For small insertion/deletion probabilities, e.g., $p_l = p_d = 1.0 \times 10^{-3}$, substitution errors with probability $p_s = 2.0 \times 10^{-2}$ have a dominant contribution to the decoded error rates, and hence error rates do not fall sharply.

(5) Error Rates of Low-Rate Codes with Large Length

Figure 11 shows the error rates of low-rate codes with $R =$
Table 1  Effect of frozen bit values on decoded error rate ($p_s = 0, n = 12, R = 0.80, L = 16$).

<table>
<thead>
<tr>
<th>$p_s$</th>
<th>BLER</th>
<th>BER</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>(a)</td>
<td>(b)</td>
</tr>
<tr>
<td>0.009</td>
<td>$1.3 \times 10^{-3}$</td>
<td>$1.2 \times 10^{-4}$</td>
</tr>
<tr>
<td>0.010</td>
<td>$5.2 \times 10^{-3}$</td>
<td>$5.1 \times 10^{-4}$</td>
</tr>
<tr>
<td>0.011</td>
<td>$1.7 \times 10^{-2}$</td>
<td>$1.9 \times 10^{-3}$</td>
</tr>
<tr>
<td>0.012</td>
<td>$5.2 \times 10^{-2}$</td>
<td>$5.1 \times 10^{-3}$</td>
</tr>
</tbody>
</table>

Table 1 lists the error rates for three cases of frozen bit values, where the values are determined as (a) $u_i = 0$, (b) $u_i = 1$, and (c) $u_i \in \mathbb{B}$ with $p(u_i = 0) = p(u_i = 1) = 1/2$, for $i \in A^c$. Here, the channel and code parameters are determined as $p_s = 0, n = 12, R = 0.80$, and $L = 16$. The results do not show significant dependence of error rates on the frozen bit values, and thus it is expected that the IDS channel is symmetric with respect to input bit value.

6. Conclusion

This paper presented an SC decoding of polar codes modified for IDS error channels, in which insertions and deletions are expressed by drift values. The recursive computations of original SC decoding is modified to include drift values in the calculation. The computational complexity of the SC decoding is $O(D^3)$ with respect to the maximum drift value $D$, and $O(N \log N)$ with respect to the code length $N$. This also showed a simulation based estimation of symmetric capacity of polar bit channels to determine the positions of frozen bits. Simulation results showed that the presented SC list decoding with CRC gives lower decoded error rates compared to the existing codes, such as spatially-coupled code, and concatenated coding with LDPC code and synchronization marker. Future works include reduction of decoding complexity by approximation, theoretical analysis and proof of polarization of polar bit IDS channel, theoretical analysis on the decoded error rate of the modified SC list decoding, and efficient determination method of frozen bits.

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References

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