Highly efficient high-power rectenna with the diode on antenna (DoA) topology

Kenji Itoh†, Member, Naoki Sakai†, Member, Keisuke Noguchi†, Member

SUMMARY In this paper, a high-efficiency high-power rectenna with a bridge diode and the diode on antenna (DoA) topology is discussed. First, the topologies of rectifiers and rectennas are discussed to indicate the direction for obtaining highly efficient rectification. Rectifiers with well-matched diode pairs, as double voltage and bridge rectifiers, can reactively terminate even order harmonics, and is suitable for highly efficient operation. A rectenna with the DoA topology is suitable for a direct connection between the highly functional antenna and the rectifier diodes to remove lossy circuit portions. Next, the formulas for the rectification efficiency of the bridge rectifier are demonstrated with the behavioral model. The indicated formulas clarify the fundamental limitation on the rectification efficiency, which is the design goal in case of the DoA topology. Finally, we demonstrate a 5.8 GHz band 1 W rectenna with the bridge diode and the DoA topology. The bridge rectifier that is directly connected to the inductive high-impedance antenna achieved a rectification efficiency of 92.8% at an input power of 1 W. This is close to the fundamental limitation due to the diode performance.

key words: Wireless power transfer, Rectenna, Rectifier, Diode, Antenna

1. Introduction

Wireless power transmission systems in microwaves (MPTs) have been studied since the 1960s for solar power satellite systems (SPS) [1]–[4]. In addition to SPS, MPT systems have been studied for unmanned aerial vehicles, such as drones and airships [5], [6]. High-power transmission technologies with high efficiency are required to expand the applications of MPT systems. At present, the “Internet of Things” era requires social implementations of “real wireless systems without power lines”.

In high-power MPT systems, the hardware bottleneck of the transfer efficiency is a rectenna with a rectenna and a rectifier. Thus, a highly efficient rectenna is the key component. Antennas, circuits, and semiconductor techniques to improve efficiencies in 1 to 10 W-class rectifiers/rectenna have been studied for the above applications [1], [2], [4], [5].

In this paper, high-power rectifiers and rectennas are discussed for highly efficient MPT systems. And, the bridge rectifiers are focused, because of the advantage of highly efficiencies with well-matched diode pairs. Our concentration on bridge rectifiers [7]-[11] is due to the previous research on diode mixers, such as even harmonic mixers [12] or ring mixers [13]. Even in millimeter-waves, mixers with well-matched diode pairs maintain their basic behaviors related to the termination of harmonics. This feature of well-matched diode pairs can be realized on the rectification and even-order harmonics’ reaction using bridge diodes. In the following discussions, we provide an overview of high-power rectifier/rectenna topologies and describe our work on the formulation of the rectification efficiency with the behavioral model and the implemented rectenna.

2. High Power Rectifier/Rectenna Topologies and the DoA Topology

Fig. 1 shows the rectifier topologies in previous studies. The most common topology is the single-shunt rectifier, as shown in Fig. 1 (a) [1], [2], [5], [14]. This rectifier topology employs a harmonic reaction circuit for full-wave rectification with a single diode. In the 1970s, a 2.4 GHz band single shunt rectifier with a GaAs Schottky barrier diode (SBD) achieved a rectification efficiency of 90% at an input power of 8 W [14]. Double voltage rectifiers [15] and bridge rectifiers [14],[7]-[11] contain several rectifier diodes, as shown in Figs. 1 (b) and (c), respectively. Both rectifiers employ matched diodes for the reactive termination of even-order harmonics. Thus, they are suitable for monolithic integration as they contain fewer circuit components than single-shunt rectifiers. In the 1960s, a 2.4 GHz band bridge rectifier for SPS applications was reported [14], after which there have been fewer reports on high-power rectification with bridge rectifiers. In the 2010s, we reported bridge rectifiers with a focus on their miniaturization and high-efficiency, because of well reactive termination on even-order harmonics [7]–[11]. Even today, bridge rectifiers are mainly applied to low-power utilizations for energy harvesting. However, a high-power bridge rectifier was reported in [16] in addition to our studies mentioned above. Figs. 1 (d) and (e) indicate field effect transistor (FET) rectifier topologies, instead of diode rectifiers. As there are only a few commercial diodes used for high-power rectifications, synchronous rectifiers configured with an FET amplifier as a rectification device were proposed, as shown in Fig. 1 (d) [17], [18]. The topology enables high-power operation owing to the high current density of the FETs. Compared with diode rectifiers, the reported rectification efficiencies are relatively low because of the internal/external feedback mechanism for gate switching.

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Another FET rectifier topology is the gated anode diode (GAD) topology configured with the enhancement mode FET, as shown in Fig. 1(e) [19]–[21]. The gate and drain electrodes of the FET are connected to configure the GAD that behaves as a diode. This is a common circuit technique for low-power CMOS rectifiers for radio-frequency identification (RFID) [19]. For high-power rectification, the higher current density of the FET compared to that of the SBD makes the same high-power performance as the reported synchronous rectifiers, and the same circuit topology as the diode rectifier results in highly efficient performance. For high-power rectification, we reported a SOI-CMOS bridge rectifier [20] and a GaAs enhancement mode pseudomorphic-HEMT (E-PHEMT) double-voltage rectifier [21]. The developed 5.8 GHz band GaAs rectifier MMIC shown in Fig. 2 achieved the rectification efficiency of 77.9% at the input power of 37.1 dBm, with a 0.5 µm GaAs E-PHEMT GAD. Furthermore, we reported a GaN HEMT GAD with a breakdown voltage of 40 V for 10 W class rectification [22].

Fig. 3 indicates the functional block diagrams of the rectennas. In the conventional rectenna shown in Fig. 3 (a), antennas and rectifiers are designed independently and are connected to each other through the 50 Ω interface. Rectifier diodes can be represented as a parallel circuit with resistance \( R_{\text{rec}} \) and capacitance \( C_{\text{rec}} \). Typically, \( R_{\text{rec}} \) is higher than 50 Ω with a DC load resistance optimized for efficiency improvements. Thus, a rectenna includes the circuit functionalities of the impedance transforming to the high rectenna resistance \( R_{\text{rec}} \), matching inductance \( L_{\text{m}} \), and harmonic reaction. Implemented circuits for the above functionalities include insertion losses that result in efficiency degradation. To improve the efficiency, a high antenna impedance topology was proposed for the rectenna, as shown in Fig. 3 (b). As a high-impedance antenna, the folded dipole antenna (FDA) with an input impedance of 470 Ω is employed for the rectenna, as shown in Fig. 4 [8].

For FDAs, impedances higher than 1 kΩ can be realized without radiation efficiency degradation [23]. In the developed rectifier, the functionality of the impedance transformation is realized in the FDA. The additional functionalities of the harmonic reaction and matching inductance are configured with the L-type low pass filter. The developed 2.4 GHz band bridge rectifier achieved a rectification efficiency of 81% at an input power of 29 dBm, with commercial Si SBDs (breakdown voltage: \( V_{\text{br}} = 22.7 \) V; series resistance: \( R_s = 7.5 \) Ω; junction capacitance at the bias voltage of 0 V: \( C_0 = 0.7 \) pF) [8].

For further improvements, a high frequency and an anti-resonant circuit that realizes all functionalities of the RF front-end was proposed to implement a direct connection between the antenna and the rectifier diode, as shown in Fig. 3 (c). This is the proposed DoA topology implemented to improve the rectification efficiency of the high-power rectifier, as shown in Fig. 5 [10]. An inductive high-impedance FDA and harmonic reaction feeders (HRFs) are integrated to realize all circuits’ functionalities of the RF front-end. There are no lossy circuit components between the antenna and the rectifier diodes. And the parallel resonance circuit is configured with the overall rectenna. Thus, there is no standing wave at the resonance frequency. This DoA topology minimizes the loss of the interconnection and maximizes the rectification efficiency, as described in Section 4. The developed 2.4 GHz band rectifier achieved a rectification efficiency of 91.5% at an input power of 31 dBm, with packaged GaAs SBDs (\( V_{\text{br}} = 27 \) V; \( R_s = 0.9 \) Ω; \( C_0 = 0.6 \) pF) [10].

Fig. 6 indicates the state of the art on rectification efficiencies of rectifiers in the 2.4 GHz and 5.8 GHz bands. With the DoA topology, high rectification efficiencies can be achieved especially in 5.8 GHz band. Owing to the increment in the circuit loss, efficiency improvement is clearly realized in the higher frequency bands.
3. Fundamental Limitation on the Rectification Efficiency of the Bridge Rectifier [24]

With the DoA topology, the circuit loss can be minimized to zero. Thus, the limitation on the rectification efficiency restricted by the rectifier diode performance can be realized with this topology. In this section, formulation of the fundamental limitation on the rectification efficiency of the bridge rectifier using the novel behavioral model is described. The derived formulas can be applied to the codesign of the antenna and rectifier.

3.1 Behavioral Model

Fig. 7 indicates a functional block diagram of the bridge rectifier. The circuit functionalities of the bridge rectifier consist of the impedance transform, the harmonic reactive termination for odd-order harmonics, the matching inductance \( L_m \), the bridge diode, and the smoothing capacitance \( C_l \). The DC output current is outputted to the load resistance \( R_o \). The smoothing capacitor \( C_l \) reactively terminates the even-order harmonics generated by the bridge diode.

Fig. 8 illustrates the behavioral model of a bridge rectifier. The behavioral model consists of the following functional blocks:

i) Impedance transform, which tunes the RF impedance \( nR_o \).

ii) RF impedance matching with loss \( L_{rf} \). Impedance matching was formulated with the RF impedance network of the bridge rectifier.

iii) Rectification with efficiency \( \eta_{rec} \). Rectification behavior is formulated with the rectifier current waveform obtained from the switch model of the SBD [12].

In the behavioral model, fully sufficient conditions for RF impedance matching and harmonics' recovery to DC are assumed to clarify the fundamental limitation on the rectification efficiency. Lossy components in the matching circuit are excluded from the behavioral model. Thus, the formulated rectification efficiency is only restricted by the diode performance.

In the behavioral model, the overall rectification efficiency
where $P_m$ is the input power; $P_{in}$ is the reduced input power due to $L_{fr}$ and $P_{dc}$ is the DC output power.

### 3.2 Formulation

Fig. 9 (a) illustrates the RF equivalent circuit of the bridge rectifier. The bridge diode is represented as the RF resistances $R_{rec}$, $R_s$, and capacitance $C_d$. $R_{rec}$ is the switching resistance of the bridge diode, and $C_d$ and $R_s$ are the parasitic components of the bridge diode. The loss of the matching inductance $L_m$ was excluded, as mentioned. $C_d$ is approximated as

$$C_d = C_d \left[1 + \frac{|V_{dc}|^2}{(2V_i)^2} \right]^{-0.5} + C_p,$$  \hspace{1cm} (2)

where $V_{dc}$ is the built-in voltage; $C_{jo}$ is the zero-biased junction capacitance; $C_p$ is the parasitic capacitance of the SBD, and $V_{dc}$ is the output DC voltage of the bridge rectifier. This equation is a modified form of the equation for a single-shunt rectifier [25]. The matching inductor $L_m$ is designed for conjugate matching with $C_d$ to achieve the maximum RF power at $R_{rec}$. Thus, $V_m$ at $f_0$ becomes $R_m$ as follows:

$$R_m = \frac{1 + \mu^2 R_s}{(\pi f_0)^2} \approx \frac{1}{\mu^2 R_s}, \quad \mu = 2\pi f_0 \approx \frac{1}{\sqrt{\pi C_d L_m}}. \hspace{1cm} (3)$$

For the matching condition shown in Fig. 9 (b), $R_{rec}$ and the power $P_{in}$ that is consumed at $R_{rec}$ are given as follows:

$$R_{rec} = R_m \cdot nR_0 \left( \frac{R_m - nR_0}{R_m} \right), \quad P_{in} = \left( \frac{R_m - nR_0}{R_m} \right) P_m. \hspace{1cm} (4)$$

The matching loss $L_{fr}$ in this RF model is given by

$$L_{fr} = \frac{P_m}{R_m - nR_0} = \frac{nR_0}{R_m - nR_0}. \hspace{1cm} (5)$$

As indicated in Eq. 5, a higher diode capacitance $C_d$ or RF impedance $nR_0$ results in a higher matching loss $L_{fr}$.

Next, the rectification efficiency $\eta_{rec}$ is formulated using the DC characteristics of the SBD. The SBD can be represented by the switches shown in Fig. 10. The SBD is modeled as the resistor $R_s$ and ideal switches $SW_f$ and $SW_b$. Switch $SW_f$ turns on for $V_i < V_j$, and switch $SW_b$ turns on for $V_j < V_{br}$, as shown in Fig. 10 (b). The efficiency is analyzed with the circuit model indicated in Fig. 11 (a). The output current $I_{out}$ indicated in Fig. 11 (b) and load resistance $R_L$ give the overall output DC power $P_{dc}$ including that of the harmonics.

$$P_{dc} = R_s \cdot \frac{1}{\pi} \int_{0}^{\pi} \left| I_{out} \right|^2 d\theta = 8nR_oR_m P_m \left( \frac{-\theta}{2} \right) \left[ 1 + \frac{V_i^2}{n \cdot R_s \cdot P_m} \right] - \frac{3V_i}{\sqrt{2n \cdot R_s \cdot P_m} \cdot \cos \theta \cdot \sin \left( \frac{\pi}{2} \right)} \right] \left[ 1 + \frac{V_i^2}{n \cdot R_s \cdot P_m} \right] - \frac{3V_i}{\sqrt{2n \cdot R_s \cdot P_m} \cdot \cos \theta \cdot \sin \left( \frac{\pi}{2} \right)} \right]. \hspace{1cm} (6)$$

It is assumed that all harmonics of the output current $I_{out}$ are converted into DC. As described, we insert the resistance $R_m$, which represents the matching loss $L_{fr}$. The RF impedance, $nR_0$, of the rectifier is reduced by $R_m$. In general, $R_m$ has an extremely high resistance compared to that of $nR_0$. The approximation $nR_0R_m \left( nR_0 + R_m \right) \approx nR_0$ can be used in Eq. 6. From Eq. 6, the rectification efficiency $\eta_{rec}$ is given by

$$\eta_{rec} = P_{dc} \left( P_m \right) \left( \frac{\pi}{2} \right) \left[ 1 + \frac{V_i^2}{n \cdot R_s \cdot P_m} \right] - \frac{3V_i}{\sqrt{2n \cdot R_s \cdot P_m} \cdot \cos \theta \cdot \sin \left( \frac{\pi}{2} \right)} \right] \left( \frac{\pi}{2} \right) \left[ 1 + \frac{V_i^2}{n \cdot R_s \cdot P_m} \right] - \frac{3V_i}{\sqrt{2n \cdot R_s \cdot P_m} \cdot \cos \theta \cdot \sin \left( \frac{\pi}{2} \right)} \right]. \hspace{1cm} (7)$$

At $\frac{\partial \eta_{rec}}{\partial R_m} = 0$, the load resistance $R_L$ for the maximum rectification efficiency $\eta_{rec}$ is given as $R_L = n \cdot R_0 + 2R_s$.
With the $R_t$ condition, $\eta_{\text{rec}}$ can be expressed as:

$$\eta_{\text{rec}} = \frac{2}{\pi} \left\{ \left(1 - \frac{2R_t}{nR_0 + 2R_t}\right) \left(\frac{\pi}{2} - \theta\right) \left(1 + \frac{V_i}{nR_0 \cdot P_m'}\right) - \frac{3V_i}{\sqrt{2nR_0 \cdot P_m'}} \cos \theta \right\}$$

(8)

This formula indicates that the rectification efficiency $\eta_{\text{rec}}$ can be improved with a higher RF impedance, $nR_0$.

Next, the maximum input power is discussed. At first, the maximum input power $P_{\text{inmax}}$ of the rectification block shown in Fig. 8 is formulated. $P_{\text{inmax}}$ is restricted by the breakdown of the SBDs in the off state. A higher $n$ results in a higher terminal voltage $V_d$ of the SBDs in the off state. This results in a lower $P_{\text{inmax}}$ value. $V_d$ is given by the voltage drop with the rectified DC current $I_{\text{rms}}$ through the load resistance $R_t$, and the SBD in the on state. With the assumption of $\sqrt{n} \cdot V_{\text{in}} \gg 2V_i$, $V_d$ is approximated as

$$V_d = \frac{\sqrt{n} \cdot (R_0 + R_t)}{2\sqrt{2} \cdot R_t} \cdot V_{\text{in}} + V_i.$$  (9)

At the breakdown voltage $V_d = V_{\text{br}}$, $P_{\text{inmax}}$ under the condition of $R_t = n \cdot R_0 + 2R_t$ is derived as follows:

$$P_{\text{inmax}} = \frac{(nR_0 + 2R_t)^2 \cdot (V_{\text{br}} - V_i)^2}{nR_0 \cdot (nR_0 + 3R_t)^2}.$$  (10)

With the assumptions of $nR_0 \gg R_t$ and $V_{\text{br}} \gg V_i$, $P_{\text{inmax}}$ can be approximated as

$$P_{\text{inmax}} = \frac{V_i^2}{nR_0}.$$  (11)

This equation provides a good design guideline for the RF impedance $nR_0$. A higher RF impedance, $nR_0$, lowers $P_{\text{inmax}}$, as discussed above.

Finally, the overall characterization of the bridge rectifier is discussed. Substituting Eq. 5 and Eq. 8 into Eq. 1, the overall rectification efficiency $\eta$ is given by

$$\eta = \frac{\eta_{\text{rec}} \cdot L_t}{(1 - \omega^2 C_d^2 R_s \cdot nR_0)} \cdot \left[ \frac{2}{\pi} \left(1 - \frac{2R_t}{nR_0 + 2R_t}\right) \left(\frac{\pi}{2} - \theta\right) \left(1 + \frac{V_i}{nR_0 \cdot P_m'}\right) - \frac{3V_i}{\sqrt{2nR_0 \cdot P_m'}} \cos \theta \right]\right].$$

(12)

As already discussed, the matching loss $L_t$ and rectification efficiency $\eta_{\text{rec}}$ have opposite dependences on the RF impedance $nR_0$, as shown in Fig. 12 (a). This indicates that there is an optimum RF impedance $nR_0$ that can be determined in the design procedure. With the optimum RF impedance $nR_0$, the theoretical rectification efficiency is a fundamental limitation that is restricted by the diode performance. From Eq. 5 and Eq. 10, the overall maximum input power $P_{\text{inmax}}$ is given by

$$P_{\text{inmax}} = L_t \cdot P_{\text{inmax}}' = \frac{1}{1 - \omega^2 C_d^2 R_s \cdot nR_0} \cdot \left(\frac{nR_0 + 2R_t}{nR_0 \cdot (nR_0 + 3R_t)^2}\right) \cdot \left(V_{\text{br}} - V_i\right)^2.$$  (13)

As shown in Fig. 12 (b), Eq. 13 represents the behavior of $P_{\text{inmax}}$ versus the RF impedance $nR_0$. The initial design of $nR_0$ can be established using Eq. 12 and Eq. 13.

3.3 Experimental investigations

To confirm the effectiveness of the behavioral model and derived closed-form formulas, the theoretical, simulated, and measured values are summarized in Table I for the 2.4 GHz band 10 W-class rectifier [24] and the 5.8 GHz 1 W rectifier [11] described in Section 4. The theoretical rectification efficiencies $\eta$ at the maximum input powers $P_{\text{inmax}}$ agree well with the measured values at 2.4 and 5.8 GHz. Thus, experimental investigations confirm the effectiveness of the behavioral model and derived closed-form formulas.
Table 1  Theoretical, simulated, and measured rectifier performances.

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<tr>
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<th>[24]</th>
<th>[11]</th>
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<tr>
<td>Frequency $f_0$ (GHz)</td>
<td>2.4</td>
<td>5.8</td>
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<tr>
<td>RF impedance $nR_a$ (Ω)</td>
<td>250</td>
<td>580</td>
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Model parameters of GaAs SBDs

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<td>$V_r$ (V)</td>
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<tr>
<td>$R_a$ (Ω)</td>
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</tr>
<tr>
<td>$C_0$ (pF)</td>
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</tr>
<tr>
<td>$C_s$ (pF)</td>
<td>0.1</td>
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<tr>
<td>$V_s$ (V)</td>
<td>61.4</td>
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Simulated values by the harmonic balance method with circuit schematics of Fig. 7

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<tr>
<td>$\eta$ (%)</td>
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<td>$P_{\text{max}}$ (dBm)</td>
<td>40.5</td>
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Measured values without additional circuit losses

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<td>$\eta$ (%)</td>
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<tr>
<td>$P_{\text{max}}$ (dBm)</td>
<td>40.8</td>
</tr>
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</table>

4. 5.8 GHz Band Rectenna with the DoA topology [11]

In this section, we discuss a highly efficient 5.8 GHz band 1 W rectenna with the DoA topology. The rectenna consists of a GaAs bridge diode and a stub connected high-impedance dipole antenna having an antenna resistance of 580 Ω.

4.1 Configuration

Fig. 13 indicates the configuration of the 5.8 GHz band 1 W rectenna with the DoA topology. The highly functional antenna consists of a high-impedance dipole antenna, a short stub, and harmonic reaction feeders (HRFs). The bridge diode is directly connected to the antenna through the HRFs. On the reverse side of the substrate, there is a short stub connected high-impedance dipole antenna. On the surface side, there are HRFs, a bridge diode, and a smoothing capacitor.

Fig. 14 indicates the RF equivalent circuit of the antenna. The conductance $G_s=1/R_s$ and susceptance $B_s$ represent the dipole antenna admittance $Y_s$, and $B_s$ represents the short stub susceptance. The conductance $G_{\text{rec}}$ and capacitance $C_{\text{rec}}$ represent the RF equivalent circuits of the bridge diode. Between the short stub connected high-impedance dipole antenna and the bridge rectifier, there are HRFs composed of an inductor $L_s$ and two transmission lines $T_1$ and $T_2$, as shown in Fig. 14 (b). The HRF is designed such that the input impedance $Z_t$ is short at $f_0$ and open at $3f_0$. At $f_0$, the bridge diode is directly connected to the short stub connected high-impedance dipole antenna without any lossy circuit components. This indicates that $Y_{\text{ant}} = Y_s + jB_s$ in Fig. 14 (a). A parallel resonant circuit is configured with the antenna $Y_{\text{ant}}$ and the bridge diode $Y_{\text{rec}}$, as shown in Fig. 14 (a). There is no standing wave at the resonant frequency $f_0$ between the antenna and the diodes. This DoA topology can minimize the interconnection loss.

Fig. 15 illustrates the simulated dipole antenna admittance $Y_s$ and the bridge diode’s admittance $Y_{\text{rec}}$ at the 5.8 GHz band. $R_s$ increases with increasing element length $H_{\text{ele}}$ of the dipole antenna from 0.15λ₀ to 0.35λ₀, where λ₀ is the wavelength at $f_0$. #A in the $Y_s$ locus on $H_{\text{ele}}$ indicates that $R_s = 50$ Ω of the conventional dipole antenna with $H_{\text{ele}}$ of 0.17λ₀. #B indicates that $R_s = 580$ Ω of this high-impedance dipole antenna with an extended $H_{\text{ele}}$ of 0.3λ₀. The antenna conductance $G_{\text{ele}}=1/R_s$ is designed to be equal to the bridge diode’s conductance $G_{\text{rec}}$. The antenna susceptance $B_s$ is lower than that of the bridge diode’s conjugate susceptance $-B_{\text{rec}}$. To obtain a conjugate matching to $Y_{\text{rec}}$, a short stub with susceptance $B_s$ is connected in parallel to the dipole antenna. The susceptance $B_s + B_{\text{rec}}$ can be aligned to be the same as $-B_{\text{rec}}$. The short stub connected high-impedance dipole antenna can realize the functionalities of the impedance transform and the impedance matching. The
HRF can realize the functionalities of the third-order harmonic reaction and DC blocking.

4.2 Experimental investigations

A photograph of the 5.8 GHz band rectenna is shown in Fig. 16. The printed circuit board employs Megtron 7 [26] and a GaAs bridge SBD chip ($V_{bd} = 25$ V, $R_i = 1.7$ Ω, $C_{j0} = 0.28$ pF). Additionally, chip capacitors of 5 pF are mounted on the surface.

The measured and simulated antenna impedances $Z_{ant} = 1/Y_{ant}$ of the short stub connected high-impedance dipole antenna with HRFs are shown in Fig. 17. The extended S-parameter method [27] is employed for the measurements. The measured rectification efficiency, and (b) is shown in Fig. 18 (b). The rectenna is placed at the same position as the reference antenna. The rectification efficiency of the rectifier without antenna loss is obtained using the formula $P_{dc}/P_{in}$.

Fig. 19 indicates the characteristics of the 5.8 GHz band bridge rectifier: (a) is the rectification efficiency, and (b) is the DC output voltage $V_{dc}$. The load resistance $R_l$ is optimized to improve the rectification efficiency, and the optimized $R_l$ is 890 Ω. The measured rectification efficiency is 92.8% at an input power $P_{in}$ of 30 dBm. Both the measured and simulated values are in good agreement, with a difference of less than 1%. The measured rectification efficiency is in good agreement with the fundamental limitation of 92.2%, as shown in Table I.

Fig. 20 indicates the azimuth pattern of the measured and simulated antenna performances of the short stub connected high-impedance dipole antenna with HRFs. The measurement setup is the same as that shown in Fig. 18 (b). The antenna gain is obtained from the difference in the input power between the standard dipole antenna and the rectenna. For the input power measurement, the standard dipole antenna is placed at the same position as that of the rectenna, as shown in Fig. 18 (b). The input power of the rectenna is estimated from the measured $V_{dc}$ versus input power $P_{in}$, as shown in Fig. 19 (b). The measured antenna gain of 2.3 dBi at an azimuth of 90° is lower than the simulated one of 2.4 dBi. The measured antenna radiation efficiency can be estimated from the gain difference of 0.1 dB, under the assumption that the reference antenna is placed at the same position as that shown in Fig. 1.
assumption of the same antenna radiation patterns in the simulation and measurement. The measured antenna radiation efficiency of 96.9% is estimated from the simulated value of 99.2% and includes losses due to the circuit functionalities and interconnection between the antenna and the bridge diode. The proposed DoA topology, which is the direct connection between the proposed antenna and the bridge diode, eliminates the additional loss due to lossy circuit components under the standing wave by interconnection.

As shown in Fig. 6 (b), the rectifier in our study achieved the highest rectification efficiency of 92.8% among the 5.8 GHz band rectifiers.

Based on the above discussion, the effectiveness of the proposed DoA topology can be confirmed via experimental investigations.

![Fig. 18](image-url) Measurement setup for rectification efficiency of the rectifier: (a) input power measurement, and (b) DC output power measurement.

![Fig. 19](image-url) Characteristics of the 5.8 GHz band bridge rectifier: (a) rectification efficiency, and (b) DC output voltage $V_{dc}$.

5. Conclusion

In this paper, high-power rectifiers and rectennas were discussed for highly efficient high-power rectification in MPT systems. Topologies of reported rectifiers and rectennas were summarized, and a rectenna with the DoA topology was confirmed via experimental investigations.

![Fig. 20](image-url) Azimuth pattern of measured and simulated antenna performances of the short stub connected high-impedance dipole antenna with the HRFs: (a) measured DC output voltage of the rectenna, and (b) topology was proposed to minimize circuit losses and achieve efficiency limitations. The formula for the rectification efficiency was derived to clarify the fundamental limitations. The developed 5.8 GHz band rectenna with the proposed DoA topology achieved a rectification efficiency of 92.8% at an input power of 1 W, which is close to the fundamental limitation calculated with the derived formula. The effectiveness of the proposed DoA topology was confirmed with experimental investigations.

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References


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